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	TECHNICAL CHARACTERISTICS OF LPITs FOR RGDM/RGDAT	GSCT005 Rev. 0 August 2018

TECHNICAL CHARACTERISTICS OF LPITs FOR RGDM/RGDAT

Revision	Date	List of modifications
Ed. 1	August 2018	First emission

Elaborated by		Verified by			Approved by	
GI&N/O&M/NCS	I&N Italy /TER/SMG	GI&N/O&M/NCS	I&N Italy /TER/SMG	GI&N/NTI/SMG	GI&N/O&M/NCS	GI&N/NTI/SMG
I. Gentilini	P. Paulon G. Sapienza	F. Mauri	G. Sapienza L. Delli Carpini	C. Noce	M. Mazzotti	F. Giammanco

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SCOPE

To describe common technical characteristics of Low Power Instrument Transformers to be interfaced with RGDM and RGDAT devices.

REFERENCE DOCUMENTS

IEC 61869-10 (Additional Requirements for low-power passive current transformers)

IEC 61869-11 (Additional requirements for low-power passive voltage transformers)

APPLICATIONS

LPITs to be integrated in:

- Smart Termination (e.g. DJ5400)
- Metal-enclosed indoor switchgears (e.g. DY803, DY800, GSM001, DY900)
- Stand-alone LPITs
- Pole-mounted switchgears (e.g. GSCM002, GSCM003)

COMMON CHARACTERISTICS

- Passive LPITs
- derivative or proportional LPCT
- Combined connector for LPCT and LPVT, compliant with IEC 61869-6, table 603, type RJ45, with the PIN assignments provided by Table 1003 of IEC 61869-10 and the Table 1104 of IEC 61869-11.

If the signal cable length ≥ 10 m, the RJ45 connector can be compliant to the requirements valid for cables with length < 10 m, as reported in Table 601 of the IEC 61869-6.

- Use of TEDS (for future developments)
- Following common ratings:

Rated frequency	50/60 Hz
highest voltage of equipment	36 kV
Rated power-frequency withstand voltage	70 kV
Rated lightning-impulse withstand voltage	170 kV

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Temperature category	-5/50 indoor, plus the heating due to the rated current -30/50 outdoor
Signal cable type	Twisted pair, category \geq CAT 6
Input circuit on the protection device side	<ul style="list-style-type: none"> • Balanced pure differential • Unbalanced differential • Single-ended without polarization, i.e. one terminal is connected to the electronic ground and/or to the grounding network of the substation • Single-ended with polarization, i.e. one terminal is connected to a polarization voltage and/or to a resistance that separates the terminal from the ground

LOW-POWER CURRENT TRANSFORMERS

Compliant with IEC 61869-10, with the following characteristics:

rated primary current (I_{pr})	500 A
rated secondary voltage (U_{sr})	225 mV
Secondary voltage noise level at rated primary current	0,1125 mV @500 A primary current (this noise is equivalent to 0,25 A at rated primary current)
Rated short-time thermal current (I_{th})	20 kA
Rated burden	<p>According to IEC 61869-10, the rated burden is: 20 kΩ for the resistance, and 500 pF for the parallel capacitance.</p> <p>According to IEC 61869-10, the accuracy of the LPCT, connected with its cable to the protection device, shall be guaranteed also with in the following cases:</p> <p>Resistance: \geq 20 kΩ Capacitance: \leq 500 pF</p>

Maximum common-mode voltage output	≤ 10 mV (RMS, at highest voltage of equipment, rated frequency, and no primary current).																																																																																						
Frequency Response (for derivative LPCT)	<p>The derivative LPCT frequency response shall be “high pass (HP) filter like”, with:</p> <ul style="list-style-type: none"> • linear behavior (+20 dB/dec), until the HP cutoff frequency • HP cutoff frequency ≥ 800 kHz 																																																																																						
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Rated primary time constant for transient performance (Tpr) (not for derivative LPCT)	150 ms																																																																																						
Expected life	All of the characteristics shall be guaranteed in compliance to the expected life of the application in which the LPIT is installed.																																																																																						

CURRENT SENSOR INTERNAL CIRCUIT

Inside the LPCT (linear or derivative), no additive components, passive and/or active, i.e. resistors etc., are allowed, in addition to the sensor device. Other solutions shall be approved by Enel: e.g. additive passive components may be exceptionally allowed by Enel only if the manufacturer justify, with a technical report, the benefits obtained using these components.

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The current sensor shall be completely insulated (floating) with respect to any part of the primary circuit (ground and primary wire).

The shield of the LPCT cable shall be connected to the ground only on sensor side. In case of voltage and current integrated sensors, a common shield is present, for both current and voltage sensors wires.

Moreover, all of the current sensors shields shall be connected to the ground inside of the sensor. It is never allowed to use the ground eventually taken from the protection device for grounding sensor and/or cable shields.


LPCT OUTPUT SIGNAL

The current sensor shall be completely insulated (floating) with respect to any part of the primary circuit (ground and primary wire), so the output information is a differential-mode voltage $v_{IL+,IL-}$, as reported in Fig. 1. This voltage shall be proportional to the primary current (in case of proportional LPCT) or to the derivative of the primary current (in case of derivative LPCT).

Referring to Fig. 1, voltages $v_{IL+,G}$ and $v_{IL-,G}$ can be present, e.g. due to a non-perfect and/or non homogeneous sensor shield (capacitive and/or inductive coupling). These voltages, create a common-mode voltage that can creates problems to the protection device.

For the correct operation of the protection system, it is mandatory that:

1. Both open-circuit voltages $v_{IL+,G}$ and $v_{IL-,G}$, shall be ≤ 10 mV (RMS) measured when the primary circuit is supply with the highest voltage of equipment, at rated frequency, but without any current (no load test).
2. Both open-circuit voltages $v_{IL+,G}$ and $v_{IL-,G}$, shall be “high source impedance signal type”. Practically, this means that voltages $v_{IL+,G}$ and $v_{IL-,G}$ shall go to zero when a resistive pull-down load (value: $15 \text{ k}\Omega \leq R_{\text{pull-down}} \leq 100 \text{ k}\Omega$) is connected between one and/or both wire of the current sensor ($IL+$ and/or $IL-$) and the ground. The same behavior shall occur when a balanced pull-down load is connected to the LPCT terminals, i.e. a load realized with a couple or two identical resistors (value: $15 \text{ k}\Omega \leq R_{\text{pull-down}} \leq 100 \text{ k}\Omega$) connected between both sensor wires and the ground.
3. Voltages $v_{IL+,G}$ and $v_{IL-,G}$, shall have no effect on differential-mode voltage $v_{IL+,IL-}$. This shall be valid in any regime (any steady state and any transient – e.g. during any kind of fault). This shall be valid for any type of input circuit of the protection device listed in “Input circuit on the protection device side”.
4. In any regime (any steady state and any transient), and for any type of input circuit of the protection device listed in “Input circuit on the protection device side”, the differential voltage $v_{IL+,IL-}$ shall not be influenced by spurious transient that can be generated, for example, from unwanted coupling with other part of the network, and/or from EMC issues.

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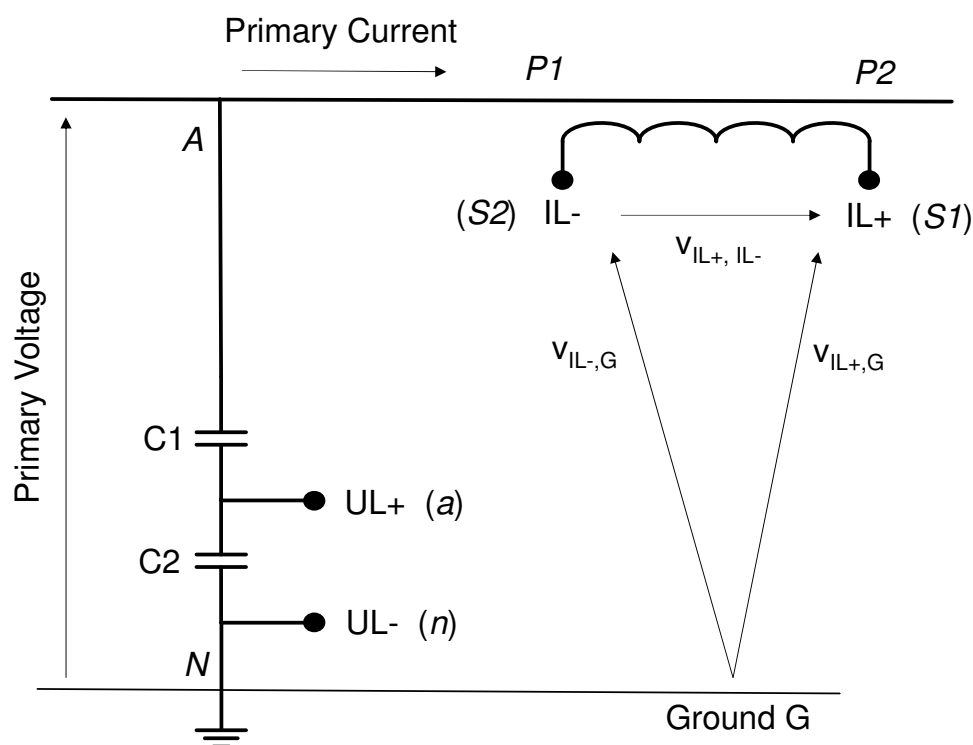


Fig. 1. General schema of an integrated voltage-current LPIT.

TESTING

The LPCT shall be tested according to Table 10 of IEC 61869-10, with the following additions:

1. the accuracy routine tests shall also include the verification of deviation requirements
2. common-mode voltage and effect on the differential voltage test, in open-circuit condition (type test)

Considering the schema of Fig. 1, the sensor shall be supply at the highest voltage of equipment, rated frequency, without any current (i.e., no load) and without any burden and pull-down load. In these conditions, both voltages $v_{IL+,G}$ and $v_{IL-,G}$ shall be ≤ 10 mV (RMS). The verification measurement shall be done with a “thru RMS” voltmeter. The voltmeter input impedance shall have these characteristics: $R \geq 10$ M Ω , $C \leq 100$ pF. In the same condition, with the same instrument, the voltage $v_{IL+,IL-}$ shall be verified. The result shall be $v_{IL+,IL-} = 0$.

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3. Common-mode voltage and effect on the differential voltage test, in pull-down condition (type test)

The previous tests, with the same supply conditions, shall be performed with a single pull-down resistance (value: $15 \text{ k}\Omega \leq R_{\text{pull-down}} \leq 100 \text{ k}\Omega$) and with a balanced pull-down load (value: $15 \text{ k}\Omega \leq R_{\text{pull-down}} \leq 100 \text{ k}\Omega$), but without any protection device connected. Measuring voltages with the same voltmeter of the previous test, the result shall be: $v_{\text{IL+,G}}$ and $v_{\text{IL-,G}}$ equal to zero, and also $v_{\text{IL+,IL-}}$ equal to zero.

4. Frequency response tests (type test)

The Manufacturer shall draw a report that contains the frequency response test. The report shall contain: the Bode diagram of the frequency response, a table that summarizes the frequency response parameter (gain, slope, cutoff frequency etc.). In case of resonances, these phenomena shall be underlined and it is necessary an explanation of the origin of the phenomena, that shall be declared and minimized.

LOW-POWER VOLTAGE TRANSFORMERS

Compliant with IEC 61869-11, with the following characteristics:

Range of Rated primary voltages Upr	$6/\sqrt{3} \div 34,5/\sqrt{3} \text{ kV}$
rated transformation ratio	10 000/1 V/V
Secondary voltage noise level at rated primary voltage	5 mV @10 000 V primary voltage (this noise is equivalent to 50 V on primary side, at rated primary voltage)
Rated voltage factor (Ku)	1,9 for 8 h
Rated burden	According to IEC 61869-11, the rated burden is: 2 M Ω for the resistance, and 50 pF for the parallel capacitance.
Capacity C2 range (see Fig. 1)	$80 \text{ nF} \leq C2 \leq 170 \text{ nF}$

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Accuracy based on individual corrected transformation ratio and corrected phase offset, with ranges and resolutions given by IEC 61869-11																																																																														
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Deviations respect to the prototype	0,1 (both for ratio (0,1%) and for phase displacement (0,1°)), at all verification points of the accuracy class																																																																													

VOLTAGE SENSOR INTERNAL CIRCUIT

Inside the LPVT, an high-accuracy capacitive divider is present. No other additive components, passive and/or active, i.e. resistors etc., are allowed. Other solutions shall be approved by Enel: e.g. additive passive components may be exceptionally allowed by Enel only if the manufacturer justify, with a technical report, the benefits obtained using these components.

The shield of the LPVT cable shall be connected to the ground only inside the sensor. In case of voltage and current integrated sensors, a common shield is present, for both current and voltage sensors wires.

Moreover, all of the LPVT shields shall be connected to the ground inside of the sensor. It is never allowed to use the ground eventually taken from the protection device for grounding sensor and/or cable shields. Also the ground point of the capacitive divider shall be connected to the ground only inside the sensor. The ground eventually taken from the protection device cannot be used to ground the capacitive divider.

TESTING

The LPVT shall be tested according to Table 10 of IEC 61869-11, with the following addition: the accuracy routine tests shall also include the verification of deviation requirements.

INTEGRATION TESTS

Enel will perform integration tests in its laboratories to verify correct operation of the whole system, i.e. LPIT connected to the RGDAT/RGDM protection devices. This shall be considered for both LPCT and LPVT.

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As example, tests that can be performed are:

1. Accuracy level, with particular reference to the measured zero-sequence current
2. Sensor impedance measurement
3. Transient response (e.g. real fault comtrades, step/dirac/ramp, etc. response)
4. Noise measurement
5. Harmonic response and accuracy level with harmonics
6. Common-mode voltage and effect on the differential voltage test, in any condition, also with a connected RGDAT/RGDM
7. Other tests that Enel considers necessary